I – Academic Planner

A. Teaching Plan (Year : 2020-21_____   Semester: Odd / Even) Odd

Teacher's Name Dr. Neena Khanijo_____   Department Physics_____

Sl. No.	UPC	Paper Name	Core/AECC/GE/SEC	Topic/Unit	Start Date	End Date
1	32221303	Digital Systems and Applications	Core	Digital circuits	17/08/20	25/08/20
2	32221303	Digital Systems and Applications	Core	Boolean algebra	26/08/20	08/09/20
3	32221303	Digital Systems and Applications	Core	Data processing circuits	09/09/20	15/09/20
4	32221303	Digital Systems and Applications	Core	Arithmetic circuits	16/09/20	26/09/20
5	32221303	Digital Systems and Applications	Core	Sequential circuits	28/09/20	06/10/20
6	32221303	Digital Systems and Applications	Core	Timers	07/10/20	12/10/20
7	32221303	Digital Systems and Applications	Core	Shift registers	13/10/20	14/10/20
8	32221303	Digital Systems and Applications	Core	Counters	20/10/20	22/10/20
9	32221303	Digital Systems and Applications	Core	Computer organization	26/10/20	03/11/20
10	32221303	Digital Systems and Applications	Core	Intel 8085 microprocessor architecture	09/11/20	23/11/20
11	32221303	Digital Systems and Applications	Core	Introduction to assembly language	24/11/20	28/11/20
12	32221303	Digital Systems and Applications	Core	Introduction to CRO	10/08/20	13/08/20

B. Outstation Field visits for students

Project Name / Paper Name			
Destination		Travel Mode	
Departure Month		Return	
Faculty-in-Charge		Number of Students going	

C. Internal Assessment: House Exam (Test/Presentation etc.) & Assignment*

Course Code	Course Name	Unique Paper Code	Topic Name	Day and Date	Date/s of Exhibiting the Assessment Sheet to students, Discussing the marks, Returning/Retaining
567	(CBCS) B.Sc.(hons.) Physics	32221303	Digital circuits	August	November
567	(CBCS) B.Sc.(hons.) Physics	32221303	Boolean algebra, Data processing circuits, Arithmetic Circuits	September	November
567	(CBCS) B.Sc.(hons.) Physics	32221303	Sequential circuits, timers, shift registers, and counters	October	November
567	(CBCS) B.Sc.(hons.) Physics	32221303	Computer organization and Intel 8085 microprocessor architecture Test	November	November

*Marks of the Internal Assessment to be submitted to the College 15 days before the last working day of every semester

I – Academic Planner

A. Teaching Plan (Year : 2020-21_____ Semester: Odd / Even) Even

Teacher's Name Dr. Neena Khanijo_____ Department Physics_____

Sl. No.	UPC	Paper Name	Core/AECC/GE/SEC	Topic/Unit	Start Date	End Date
1	32221403	Analog systems and applications	Core	Operational amplifiers	02/01/21	07/01/21
	32221403	Analog systems and applications	Core	Applications of op-amps	08/01/21	22/01/21
	32221403	Analog systems and applications	Core	D/A Conversion	23/01/21	28/01/21
	32221403	Analog systems and applications	Core	Two terminal devices and their applications	29/01/21	10/02/21
	32221403	Analog systems and applications	Core	Bipolar junction transistors	11/02/21	19/02/21
	32221403	Analog systems and applications	Core	Amplifiers	20/02/21	08/03/21
	32221403	Analog systems and applications	Core	Coupled amplifiers	09/03/21	12/03/21
	32221403	Analog systems and applications	Core	Feedback in amplifiers	15/03/21	23/03/21
	32221403	Analog systems and applications	Core	Sinusoidal oscillators	31/03/21	06/04/21
	32221403	Analog systems and applications	Core	Semiconductor diodes	07/04/21	30/04/21